

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

<b>INFORMATION DISCLOSURE STATEMENT</b>		Docket Number: <b>11283/46</b>	
Serial No. <b>To be assigned</b>	Filing Date <b>Herewith</b>	Examiner <b>To be assigned</b>	Art Unit <b>To be assigned</b>
Invention Title <b>OPERATING SYSTEM HAVING A SYSTEM PAGE AND METHOD FOR USING SAME</b>		Inventors <b>Maarten A. Koning</b>	

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 35 U.S.C. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicant hereby brings the following references to the attention of the Examiner. These references are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. The filing of this Information Disclosure Statement and the enclosed PTO Form No. 1449, shall not be construed as an admission that the information cited is prior art, or is considered to be material to patentability as defined in 37 C.F.R. § 1.56(b).
2. Copies of each of the cited references listed on the modified PTO form 1449 are not enclosed. Copies of each of the references were previously provided in the following parent application: U.S. Patent Application Serial No. 09/510,107.

Dated: 11 March 2004

By:



Michelle M. Carniaux, Reg. No. 36,098

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EXPRESS MAIL NO.: EV 321 890 583 US

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT PTO FORM 1449</b>	ATTY. DOCKET NO. 11283/46	SERIAL NO. To be assigned
	APPLICANT Maarten Koning	
	FILING DATE Herewith	GROUP To be assigned

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,557,771	September 17, 1996	Kawaguchi et al.			
	5,893,166	April 6, 1999	Frank et al.			
	5,745,418	April 28, 1998	Ma et al.			
	5,381,549	January 10, 1995	Tamura			
	5,032,981	July 16, 1991	Bril et al.			

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	0415515	March 6, 1991	EP				
	2740235	April 25, 1997	FR				
	WO 01/22209	March 2001	PCT				
	WO 97/36236	October 2, 1997	PCT				
	0327707	August 16, 1989	EP				
	09016499	January 1997	JP				
	10232786	January 1998	JP				

**OTHER DOCUMENTS**

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	“Use of Process-Spaced Address Translation for Access Control” IBM Technical Disclosure Bulletin, Vol. 37, No. 2A, February 1994
	Van Wezenbeek et al., “A Survey of Memory Management,” Microprocessing and Microprogramming 36(1993), pp. 141-162
	Invitation to Pay Additional Fees and Annex, EPO ISA under Patent Cooperation Treaty, mailed February 7, 2002 in PCT/US01/00247
	Hennessy et al., Computer Organization and Design. pp. 597 Morgan Kaufmann Publishers, Inc., 1997
	“The Design and Implementation of the Clouds Distributed Operating System” Dasgupta (Georgia Tech.), Computing Systems, Winter 1990
	“A Micro Kernel Architecture for Next Generation Processors” Okamoto (Toshiba Corp.), USENIX, 1992
	“Sharing and Protection in a Single Address Space Operating System (OPAL)” Chase (University of Washington), 1995
	Jeff Bonwick, “The Slab Allocator: An Object-Caching Kernel Memory Allocator, 1994, 12 pages, Sun Microsystems
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	Jones et al., "An Overview of the Rialto Real-Time Architecture," July 1996, 9 pages, Microsoft Research
	Mitchell et al., "An Overview of the Spring System," 1994, 10 pages, Sun Microsystems Inc.
	Khalidi et al., "The Spring Virtual Memory System," February 1993, 25 pages, Sun Microsystems Laboratories, Inc.
	Hamilton et al., "Subcontract: A Flexible Base for Distributed Programming," 1993, 11 pages, Sun Microsystems Laboratories, Inc.
	Nelson et al., "High Performance Dynamic Linking Through Caching," 1993, 14 pages, Sun Microsystems Laboratories, Inc.
	Reed et al., "Nemesis The Kernel," Overview, May 20 1997 56 pp.
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	Timothy Roscoe, "Linkage in the Nemesis Single address Space Operating System," May 1994, 8 pp., Computer Laboratory, University of Cambridge
	Richard Black, "ESPRIT LTR 21917 (Pegasus II), Deliverable 2.1.4., ARM Port Report," Jun 1997, 23 pages, IBM T.J. Watson, Research Center
	Vochteloo et al., "Protection Domain Extensions in Mungi," October 1996, 5 pp., School of Computer Science and Engineering, The University of New South Wales
	Heiser et al., "Implementation and Performance of the Mungi Single-Address-Space Operating System," June 1997, 23 pages, IBM T.J. Watson, Research Center
	Heiser et al., "Resource Management in the Mungi Single-Address-Space Operating Systems," August 1997, 15 pp., School of Computer Science and Engineering
	VxWorks® Programmer's Guide 5.3.1, Edition 1, March 4, 1997 Wind River Systems, Inc., 648 pp.
	White, Ron. "How Computers Work." ZD Press, 1997, pp. 10-13 and 84-85

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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